

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): J.M. Brennan et al.

Case: 2-81-1-4

Serial No.: 10/773,614

Filing Date: February 6, 2004

Group: 2823

Examiner: Brook Kebede

Title: Semiconductor Device Configured for  
Reducing Post-Fabrication Damage

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AMENDMENT AND RESPONSE TO OFFICE ACTION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the outstanding non-final Office Action dated November 20, 2006, please amend the above-referenced application as follows: